

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: David Arnold Luick : Date: July 29, 2008  
Group Art Unit: 2113 : IBM Corporation  
Examiner: C. S. McCarthy : Intellectual Property Law  
Serial No.: 10/667,097 : Dept. 917, Bldg. 006-1  
Filed: September 18, 2003 : 3605 Highway 52 North  
Patent No.: 7,124,318 B2 : Rochester, MN 55901  
Title: MULTIPLE PARALLEL PIPELINE  
PROCESSOR HAVING SELF-  
REPAIRING CAPABILITY

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**CERTIFICATE OF ELECTRONIC  
TRANSMISSION**

I hereby certify that this correspondence and any  
enclosures are being electronically transmitted via  
EFS-WEB on the date indicated below.

July 29, 2008

Date

/nancy j larson/

Nancy J. Larson

PETITION UNDER 37 CFR 1.183 FOR CERTIFICATE OF CORRECTION

In the matter of Patent Number 7,124,318 B2 which issued on October 17, 2006,  
a careful check of the same against the file of the attorney shows errors in the patent chargeable  
to the Official Printer, as shown on the attached Certificate of Correction.

It is requested that the attached Certificate of Correction be certified and issued to  
International Business Machines Corporation, Rochester, Minnesota 55901.

Respectfully submitted,

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Docket No.: ROC920030200US1  
Serial No.: 10/667,097

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

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PATENT NO. : 7,124,318 B2  
APPLICATION NO.: 10/667,097  
ISSUE DATE : October 17, 2006  
INVENTOR(S) : David Arnold Luick

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**IN THE CLAIMS:**

Claim 1, Column 16, Line 41: "pipelines." should be --pipelines;  
wherein said plurality of pipelines is arranged in an array;  
wherein said control logic, responsive to detection of a failure in said first pipeline, causes data intended for processing by said first pipeline to be processed by said second pipeline, said second pipeline being physically adjacent said first pipeline in said array; and  
wherein said control logic, responsive to detection of a failure in said first pipeline, further causes data intended for processing by said second pipeline to be processed by a third pipeline of said plurality of parallel pipelines, said third pipeline being physically adjacent said second pipeline in said array.--

Claim 11, Column 18, Line 3: "pipelines." should be --pipelines;  
wherein said plurality of pipelines is arranged in an array;  
wherein said control logic, responsive to detection of a failure in said first pipeline, causes data intended for processing by said first pipeline to be processed by said second pipeline, said second pipeline being physically adjacent said first pipeline in said array; and  
wherein said control logic, responsive to detection of a failure in said first pipeline, further causes data intended for processing by said second pipeline to be processed by a third pipeline of said plurality of parallel pipelines, said third pipeline being physically adjacent said second pipeline in said array.--

**MAILING ADDRESS OF SENDER (Please do not use customer number below):**

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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